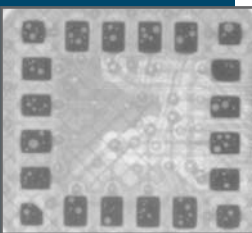
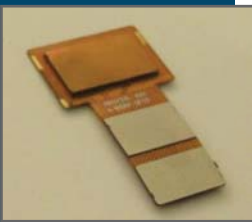
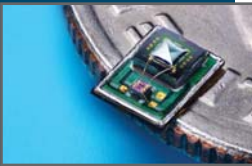
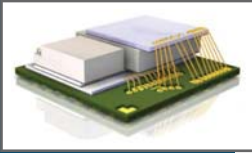


Advanced Packaging Update: Market and Technology Trends

Vol. 2-0617



This second volume of the Advanced Packaging Update provides an economic outlook for the electronics industry and describes the latest trends in fan-out wafer level packages (FO-WLP) including the new versions mounted on substrates. A special section examines the growing market for sensors in smartphones and industrial applications. TechSearch International's annual survey on substrate design rules is highlighted, with special coverage of suppliers of organic flip chip BGA and CSP substrates worldwide. The design rules include body size, core thickness, via and pad diameter, minimum bump pitch supported, and substrate finish.

Table of Contents

- 1 Industry and Economic Trends**
 - 1.1 Economic Trends
- 2 FO-WLP Developments**
 - 2.1 Reconstituted Wafer FO-WLP
 - 2.2 New FO-WLP Versions
 - 2.2.1 Nepes VF-FOP
 - 2.2.2 Huatian eSiFO
 - 2.2.3 FO-WLP on Substrate
 - 2.3 FC-CSP Alternatives
- 3 Sensor Packaging Trends**
 - 3.1 Sensors for Mobile Devices
 - 3.1.1 E-Compass Sensors
 - 3.1.2 Motion Sensors
 - 3.1.3 Pressure Sensors
 - 3.1.4 MEMS Microphones
 - 3.1.5 Fingerprint Sensors
 - 3.1.6 Proximity and Ambient Light Sensors
 - 3.2 Industrial Sensors
 - 3.2.1 Smart Factories
 - 3.2.2 Sensor Package for Industrial Applications
- 4 Substrate Design Rules**
 - 4.1 Today's Laminate Feature Size
 - 4.2 Company Design Rules
 - Access, ASE Materials, AT&S, Daeduck, Daisho Denshi, Fujitsu Interconnect Technologies, Ibiden, i3 Electronics, Kinsus, Kyocera Circuit Solutions, LG Innotek, Nan Ya PCB, SEMCO, Samsung Techwin, Shennan Circuits, Shinko Electric Industries, Simmtech, Toppan Printing, Tripod, Unimicron

Appendix: Substrate Suppliers

References

List of Figures

- 1.1. Monthly U.S. housing starts.
- 2.1. VF-FOP with double-side RDL.
- 3.1. ST's AMR magnetometer and digital compass.
- 3.2. TDK's MEMS microphone packaging.
- 3.3. Vesper's piezo vs. capacitive microphone.
- 3.4. Nepes VF-FOP.
- 3.5. ST's 3rd generation TOF ranging sensor.

List of Tables

- 2.1. Fan-out WLP Redistributed Wafer Process
- 2.2. Fan-out WLP on Substrate
- 2.3. FO-WLP Redistributed Wafer Advantages
- 2.4. Flip Chip CSP Alternatives to FO-WLP
- 3.1. Electronic Compass Sensor Packages
- 3.2. E-Compass IC Suppliers for Mobile/Wearables
- 3.3. Packages for 6-axis IMU Sensors
- 3.4. 3-axis Accelerometer/Gyroscope Packages
- 3.5. 9-axis Accelerometer/Gyroscope Packages
- 3.6. Motion Sensors for Mobile/Wearables Packages
- 3.7. Pressure Sensor Packages
- 3.8. Pressure Sensor Suppliers for Mobile/Wearables
- 3.9. MEMS Microphones Mobile/Wearables
- 3.10. MEMS Microphone Packages
- 3.11. Fingerprint Sensor Packages
- 3.12. Touch Fingerprint Sensors for Mobile Devices
- 3.13. Proximity and Ambient Light Sensors
- 3.14. Proximity and Ambient Light Sensor Suppliers
- 4.1. Build-up FC-PBGA Substrate Suppliers
- 4.2. Build-up FC-CSP Substrate Suppliers
- 4.3. Laminate PBGA/CSP Substrate Suppliers
- 4.4. Design Rules for Substrates

Annual subscription – \$4,995 (4 issues)
Single issue – \$2,500
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